



n Re the Application of:

DEC 1 2 2005

PETER L.D. CHANG

Application No.: 10/816,282

Filed: March 31, 2004

For: Memory with Split Gate Devices and

Method of Fabrication

Art Group: 2818

Examiner: Ho, Tu Tu V.

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). This IDS and IDS Citation Form are being submitted before the mailing of a final Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

It is hereby stated that no item of information contained in the Information Disclosure Statement was cited in a patent office in a counterpart application, and, to the knowledge of the undersigned, after making reasonable inquiry, no item of information contained in the Information Disclosure Statement was known to any individual associated with the filing or prosecution of the subject application more than three months prior to the filing of the Information Disclosure Statement.

12/13/2005 HDEMESS1 00000010 10816282

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The references were cited in a Search Report dated July 27, 2005 (copy enclosed herewith) in a counterpart PCT application.

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAPMAN LLI

Edwin H. Taylor, Reg. No. 25,129

Date: December 07, 2005

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I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Mail Stop Missing Parts, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Conny Willesen

Date

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Substitute for form 1449A/PTO INFORMATION DISCLOSURE	Application Number
STATEMENT BY APPLICANT	First Named Invento

(use as many sheets as necessary)

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Sheet

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of

Complete if Known		
Application Number	10/816,282	
Filing Date	March 31, 2004	
First Named Inventor	Peter L.D. Chang	
Art Unit	2818	
Examiner Name	Ho, Tu Tu V.	
Attorney Docket Number	42P18252	

	U.S. PATENT DOCUMENTS				
Examiner Initials*	Cite No.'	Document Number Number - Kind Code ² (if known)	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US-6,396,108 B1	05-28-2002	Krivokapic et al.	
		US-2002/0140106 A1	10-03-2002	Kurjanowicz	
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	FOREIGN PATENT DOCUMENTS					
Examiner	Cite	Foreign Patent Document]		Pages, Columns, Lines,	To
Initials*	No.¹	Country Code* - Number* - Kind Code* (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Where Relevant Passages or Relevant Figures Appear	
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Examiner		Date	
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'Applicant's unique citation designation number (optional). 'See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. 'Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 'For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. 'Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. 'Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08A (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003.

Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Sheet	2		of	2
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Complete if Known		
Application Number	10/816,282	
Filing Date	March 31, 2004	
First Named Inventor	Peter L.D. Chang	
Art Unit	2818	
Examiner Name	Ho, Tu Tu V.	
Attorney Docket Number	A2P18252	

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No.¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²	
		Nowak EJ, et al., "Turning Silicon On Its Edge - Overcoming silicon scaling barriers with double-gate and FinFET technology," IEEE Circuits and Devices Mag. vol. 20, no. 1, January 2004, pgs 20-31.		
		Fried, DM, et al. "Improved Independent Gate N-Type FinFET Fabrication and Characterization," IEEE Electron Device Letters, vol. 24, no. 9, September 2003, pages 592-594.		
		Fried, DM, et al. "A Fin-Type Independent-Double-Gate NFET," Device Research Conference, 2003 June, IEEE, 23 June 2003, pages 45-46.		
		PCT Search Report, Int'l application no. PCT/US2005/010164, mailed 07-27-2005, 5 pgs.		
:				

Examiner	Date
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